

Notice of References Cited	Application/Control No. 09/255,777		Applicant(s)/Patent Under Re xamination YAMAZAKI ET AL.	
	Examiner Richard A. Booth		Art Unit 2812	Page 1 of 1

U.S. PATENT DOCUMENTS

*	A	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input type="checkbox"/>	A	4057895	Nov. 1977	Ghezzeo	--	--	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	B	5920781	Jul. 1999	Imoto	438	301	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	C	4599118	Jul. 1986	Han et al.	--	--	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	D	5830787	Nov. 1998	Kim	438	163	<input type="checkbox"/>	<input type="checkbox"/>
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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								APS	OTHER
<input type="checkbox"/>	U	"DMOS FET With Common Field and Channel Doping", IBM Technical Disclosure Bulletin, Vol. 20, No. 4, September 1977, pages 1617-1620.						<input type="checkbox"/>	<input type="checkbox"/>
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*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)

**APS encompasses any electronic search i.e. text, image, and Commercial Databases.

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Notice of References Cited

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